

GA 3203

TRANSMITTAL LETTER			Case No. 7103/30
Serial No. 08/853,323	Filing Date May 8, 1997	Examiner To be Assigned	Group Art Unit 3203
(Inventor(s)) Homayoun Talieh et al.			
Title of Invention Linear Polisher and Method for Semiconductor Wafer Planarization			

TO THE ASSISTANT COMMISSIONER FOR PATENTS

Transmitted herewith is an Information Disclosure Statement and PTO Form 1449.

- Small entity status of this application under 37 CFR § 1.27 has been established by verified statement previously submitted.
- A verified statement to establish small entity status under 37 CFR §§ 1.9 and 1.27 is enclosed.
- Petition for a _____ month extension of time.
- No additional fee is required.
- The fee has been calculated as shown below:

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	Claims Remaining After Amendment		Highest No. Previously Paid For	Present Extra
Total		Minus		
Indep.		Minus		
First Presentation of Multiple Dep. Claim				

GRO		Other Than Small Entity	
Small Entity		or	
Rate	Add'l Fee	Rate	Add'l Fee
x \$11 =		x \$22 =	
x \$41 =		x \$82 =	
+ \$135 =		+ \$270 =	
total add'l fee	\$	total add'l fee	\$

- Please charge Deposit Account No. 23-1925 (BRINKS HOFER GILSON & LIONE) in the amount of \$ _____. A duplicate copy of this sheet is enclosed.
- A check in the amount of \$ _____ to cover the filing fee is enclosed.
- The Assistant Commissioner is hereby authorized to charge payment of any additional filing fees required under 37 CFR § 1.16 and any patent application processing fees under 37 CFR § 1.17 associated with this communication or credit any overpayment to Deposit Account No. 23-1925. A duplicate copy of this sheet is enclosed.
- I hereby petition under 37 CFR § 1.136(a) for any extension of time required to ensure that this paper is timely filed. Please charge any associated fees which have not otherwise been paid to Deposit Account No. 23-1925. A duplicate copy of this sheet is enclosed.

Respectfully submitted,



William A. Webb
Registration No. 28,277
Attorney for Applicant

BRINKS HOFER GILSON & LIONE
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(312) 321-4200

I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail in an envelope addressed to: Assistant Commissioner for Patents, Washington, D.C. 20231, on 18-98

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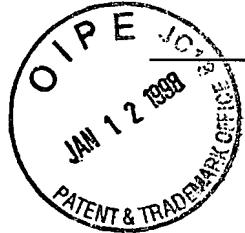
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William A. Webb

Name of Applicant, assignee or
Registered Representative


Signature



Our File No. 7103/30

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:)
Homayoun Taliyah et al.)
Serial No. 08/853,323) Examiner
Filing Date: May 8, 1997) Group Art Unit No. 3203
For Linear Polisher and method for)
Semiconductor Wafer Planarization)

INFORMATION DISCLOSURE STATEMENT

Assistant Commissioner for Patents
Washington, D.C. 20231

Dear Sir:

Applicant has enclosed copies of Form PTO 1449 listing prior art cited in parent application Serial Nos. 08/579,172 and 08/287,658. Copies of these documents can be found in the parent applications, and applicant requests that they be reviewed and made of record in this application.

Respectfully submitted,


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